



UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231
www.uspto.gov

BIBDATASHEET

CONFIRMATION NO. 6800

Bib Data Sheet

SERIAL NUMBER 10/691,706	FILING DATE 10/24/2003 RULE	CLASS 365	GROUP ART UNIT 2818	ATTORNEY DOCKET NO. 244221US2SRD DIV
-----------------------------	---------------------------------------	--------------	------------------------	--

APPLICANTS

Daisaburo Takashima, Yokohama-Shi, JAPAN;

**** CONTINUING DATA *****^{YES}**

This application is a DIV of 10/225,239 08/22/2002 PAT 6,657,882
which is a CON of 09/976,154 10/15/2001 PAT 6,473,331
which is a DIV of 09/609,058 06/30/2000 PAT 6,320,782
which is a CON of 09/208,831 12/10/1998 PAT 6,094,370
which is a CIP of 08/872,874 06/10/1997 PAT 5,903,492

**** FOREIGN APPLICATIONS *****^{NO}**

JAPAN 8-147452 06/10/1996
JAPAN 9-001115 01/08/1997
JAPAN 9-153137 05/28/1997
JAPAN 9-346404 12/16/1997

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 02/04/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 218	TOTAL CLAIMS 2	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>Phawrony TP</i> Examiner's Signature Initials				

ADDRESS

22850
OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C.
1940 DUKE STREET
ALEXANDRIA, VA
22314

TITLE

Semiconductor memory device and various system mounting them

<input checked="" type="checkbox"/> All Fees
<input checked="" type="checkbox"/> 1.16 Fees (Filing)